

PATENT NUMBER

O.I.P.E.

PATENT DATE

SCANNED LM2 Q.A. A.6

APPLICATION NO.

09/811456

CONT/PRIOR

D E

CLASS

327

SUBCLASS

108

ART UNIT

2816

EXAMINER

Long Nguyen

TITLE > APPLICANTS

Masahito 1504

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Input buffer circuit for semiconductor device

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)			
327		108		327		563 65			
INTERNATIONAL CLASSIFICATION									
H	O	B							

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DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<div style="border-bottom: 1px solid black; margin-bottom: 5px;"> (Assistant Examiner) (Date) </div> <div style="border-bottom: 1px solid black; margin-bottom: 5px; height: 100px;"></div> <div style="border-bottom: 1px solid black; margin-bottom: 5px;"> (Primary Examiner) (Date) </div> <div style="border-bottom: 1px solid black; margin-bottom: 5px;"> (Legal Instruments Examiner) (Date) </div>			NOTICE OF ALLOWANCE MAILED	
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